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Total Number of Pages in This Submission

Application Number	10/712,062
Filing Date	November 14, 2003
First Named Inventor	Shunpei YAMAZAKI et al.
Group Art Unit	2826
Examiner Name	F. Abraham
Attorney Docket Number	0756-7218

## ENCLOSURES (check all that apply)

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|   |   | <b>Remarks</b> <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees required or credit any overpayments to Deposit Account No. 50-2280 for the above identified docket number.   |

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Eric J. Robinson, Reg. No. 38,285 Robinson Intellectual Property Law Office, P.C. PMB 955 21010 Southbank Street Potomac Falls, VA 20165
Signature	
Date	October 19, 2006

## CERTIFICATE OF MAILING

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Attorney Docket No. 0756-7218



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 10/712,062

Filed: November 14, 2003

For: SEMICONDUCTOR DEVICE AND  
METHOD OF FABRICATING SAME

) Group Art Unit: 2826

) Examiner: Fetsum Abraham

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INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99,  
 Applicant submits herewith a Form PTO-1449 listing information known to Applicant and  
 requests that this information be made of record in the above identified application.  
 Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Copies of U.S. patents and U.S. publications are not enclosed in accordance with  
 the Notice published in the Official Gazette on August 5, 2003 entitled *Information  
 Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published  
 Applications in Patent Applications filed after June 30, 2003*, which waives the  
 requirement under 37 CFR 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent  
 and each U.S. publication.

An RCE was filed in this application on October 11, 2006, and no further Office  
 Action has been received. Therefore no fee is required.

Respectfully submitted,

*[Signature]*  
 Eric J. Robinson  
 Reg. No. 38,285  
 Robinson Intellectual Property Law Office, P.C.  
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 21010 Southbank Street  
 Potomac Falls, Virginia 20165  
 (571) 434-6789

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /BTL/

10712062 - GAU:

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**Complete if Known**

10/712 062

November 14, 2003

Shunpei YAMAZAKI et al.

2826

Fetsum Abraham

0756-7218

Benjamin T. Liu

U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

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**OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS**

Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/BTL/		WOLF, S., <i>Silicon Processing for the VLSI Era</i> , Volume 2, 1990, Pages 273-274, 354, 356-357, and 359.	

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if

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